

	Subclass
Class	
ISSUE CLASSIFICATION	

PAT	NUMBER

U.S. UTILITY Patent Application

1200 O.I.P.E.

PATENT DATE

SEARCHED

TRA

01/01/01

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/826035		702	118	220 2863	D. JAHNEN
APPLICANTS					
Robert Devins Paul Ferro Peter LaFauci Kenneth Mahler					
TITLE					
Method and system for logic verification using mirror interface					
BEST AVAILABLE COPY					
PTO-2040 12/89					

ISSUING CLASSIFICATION

CHOCO VERIFICATION, C.P.

CLASS      SUBCLASS      CLASS      SUBCLASS (ONE SUBCLASS PER BLOCK)

TERMINAL  
 DESIGNATED

DRAWINGS			
Scaling	Fig. Draw.	Pat. Fig.	Total Draw.
1	1	0	1

CLAIMS ALLOWED

1 Total Claims      1 Total for C.G.

The terminal is designated

EXAMINER APPROVED

(PAC)